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USB2640/40i/41/41i/49/49i Firmware Revision History
Collectively referred to in this document as the USB2640
V1.076

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Table of Contents

USB2640 Revision History.....	3
- 01 ROM.....	3
- 02 ROM.....	3

USB2640 Revision History

- 01 ROM

The -01 ROM includes all changes through 1.075

1.73: - Final QA Release

1.75: 01 Rom Mask

- Final slot logic values for SD and MS
 - Fixed the issue where firmware is initializing incorrect values for MS/SD Slot logic DELAY registers
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- 02 ROM

The -02 ROM includes all changes through 1.076

1.76: 02 Rom Mask

- Add support for SFR read/write one byte at a time. Also, allow reads of code to be 64 Bytes (actually any read, just not checked)
 - Add retries to 8-bit socket test
 - SD inserted after MSPPro causing issues. Wrong configuration of continuous modes and clocks uncomment the clearing of the force clock bit and make sure that continuous read/write is enabled
 - External dfu code updated to enable the SPI during a dfu update to resolve the issue of a blank SPI flash failing.
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